

Guest lecture on "Present and future Microprocessors"

DATE: 17/10/2020

Event Coordinator(s)

1. Prof. Tejal Deshpande

2. Prof. Kunal meher

Student Coordinator(s)

1. -

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Time & Place:

17th October,2020

9am to 10:15am

Platform: Online (Zoom)

Department:

EXTC,COMPUTER

No of participants:

Mr. Mohammed Mazhar. I. Malagi, Assistant Professor, Anjuman-I-Islam's Kalsekar Technical Campus, New Panvel delivered a Guest lecture on "Present and Future Microprocessors" on Saturday 17th Oct, 2020 for Third Year students of the Department of Electronics & Telecommunication and Computer Engineering

Because of COVID-19 pandemic situation the guest lecture was conducted on Zoom online platform.

89 participants from T.E(EXTC,Comps) attended the session. Some of the important topics covered were Advances in processor manufacturing, Advances in processor architecture, Quantum computing etc

The participants found it very informative and well organized. They look forward for more sessions on Quantum Computing

Feedback from Participants:

The session was great and interesting.

It was helpful learnt something new

Very helpful. Thank you.

great level of communication. Thank you

Session was great 👍

Very Interesting and Awesome session.

interesting session

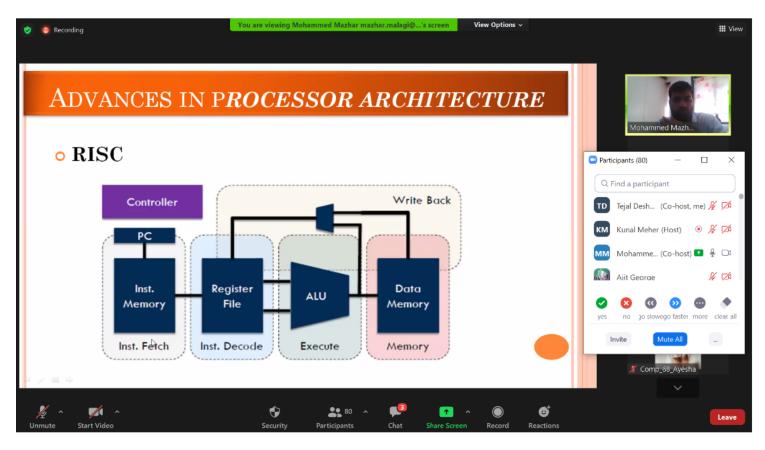
Lecture was interesting but want to know more about this topic

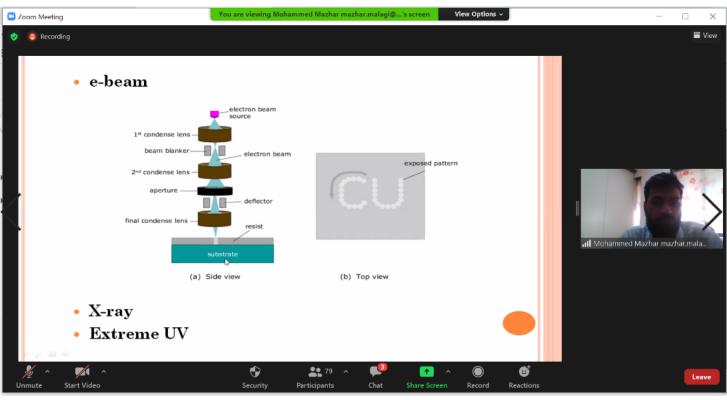
More sessions on Quantum Computing

Good session

Informative







MCQ Test

d) Final stage

1) 8086 Microprocessornumber of transistors withnm Technology
a>29000 transistors,3000nm (A)
b>3098 transistors,10000nm
c>3.1M transistors,800nm
d>5.5M transistors,500nm
2)What is Lithography?
a) Process used to transfer a pattern to a layer on the chip (A)
b) Process used to develop an oxidation layer on the chip
c) Process used to develop a metal layer on the chip
d) Process used to produce the chip
3) Silicon oxide is patterned on a substrate using
a) Physical lithography
b) Photolithography (A)
c) Chemical lithography
d) Mechanical lithography
4) Which of the following statement is not true?
a) X-ray and Electron beam lithography technique, produce device dimensions down to submicron range.
b) Ultraviolet lithography has limitation due to diffraction effects of wavelength.
c) The cost of X-ray or Electron beam is less compared to Ultraviolet photolithography (A)
d) The exposure time is less in Ultraviolet compared to X-ray or Electron beam lithography.
5) The stage in which the CPU fetches the instructions from the instruction cache in superscalar organization is
a) Prefetch stage (A)
b) D1 (first decode) stage
c) D2 (second decode) stage

6) In the execution stage the function performed is
a) CPU accesses data cache
b) executes arithmetic/logic computations
c) executes floating point operations in execution unit
d) all of the mentioned (A)
7) Which of the following makes Qubit different than a classical bit?
a>Superposition of states
b>Entanglement
c>Both of the above (A)
8)A Qubit can be in of both the states at the same time
a>Entanglement
b>Superposition (A)
9) A Qubit is a quantum mechanical system
a>2-state (A)
b>3-state
c>4-state
10)sphere is a representation of a qubit, the fundamental building block of quantum computers
a>Bloch (A)
b>Hilbert
c>Hermitian

Points Scored in MCQ



Median 16 / 20 points

Range 2 - 20 points

Total points distribution

